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| APPL NO.   | FILING OR 371<br>(c) DATE | ART UNIT | FIL FEE REC'D | ATTY. DOCKET NO | DRAWINGS | TOT CLMS | IND CLMS |
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| 10/712,150 | 11/13/2003                | 2133     | 900           | 2008.001982     | 8        | 19       | 2        |

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DOCKETED ☒ UPDATE ☒  
Previously Not Required  
Action Required Filing  
Receipt Rec'd  
Updated 4/10/04  
Pub. Info. 4/10/04  
App. Admin. Info. 4/10/04  
By J Checked A

CONFIRMATION NO. 8235

## UPDATED FILING RECEIPT



\*OC000000012221591\*

Date Mailed: 03/30/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

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DOCKETING DEPT.

APR 01 2004

WILLIAMS, MORGAN &amp; AMERSON

## Assignment For Published Patent Application

MICRON TECHNOLOGY, INC.;

## Domestic Priority data as claimed by applicant

This application is a DIV of 09/376,786 08/18/1999

## Foreign Applications

If Required, Foreign Filing License Granted: 02/09/2004

Projected Publication Date: 07/08/2004

Non-Publication Request: No

Early Publication Request: No

## Title

memory  
Compression circuit for testing a memory device

**Preliminary Class**

714

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Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:  
TODD A. MERRITT  
NICHOLAS VANHEEL

Group Art Unit:

Examiner:

Serial No.: 10/712,150

Conf. No.: 8235

Filed: NOVEMBER 03, 2003


Atty. Dkt.: 2008.001982/98-0303.01  
CUSTOMER NO.: 23720

For: COMPRESSION CIRCUIT FOR  
TESTING MEMORY DEVICE

**REQUEST FOR CORRECTED FILING RECEIPT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

| CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8   |                |
|--|----------------|
| DATE OF DEPOSIT:   | April 27, 2004 |
| I hereby certify that this paper or fee is being deposited with the United States Postal Service with sufficient postage as "FIRST CLASS MAIL" addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. |                |
| <br>Signature   |                |

A corrected filing receipt is hereby requested in view of the error that appears in the original. For the convenience of the Patent and Trademark Office, attached is a photocopy of the original receipt on which the error has been noted in red.

As the error is not due to any error by Applicants, no fee is believed to be due in connection with the filing of this document. However, should any fee under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to this document, the Assistant Commissioner is hereby authorized to deduct said fee from Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2008.001982/98-0303.01.


Please date stamp and return the enclosed postcard evidencing receipt of these materials.

Respectfully submitted,

WILLIAMS, MORGAN & AMERSON, P.C.  
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Date: April 27, 2004

By: \_\_\_\_\_

  
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